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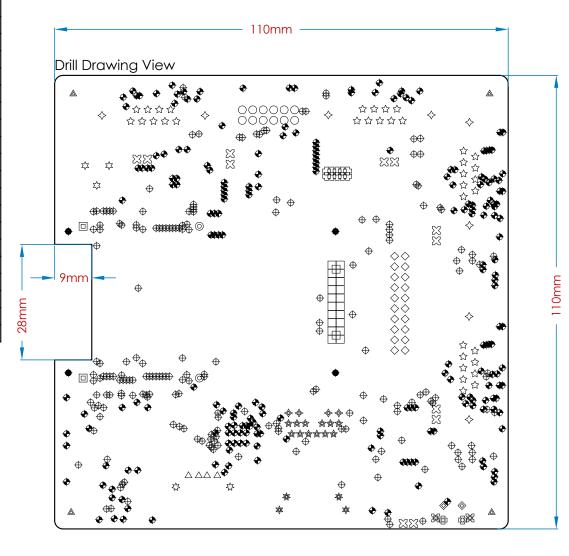
## Note:

- 1. All PCB Fabrication note dimensions are in mm unless otherwise specified
- 2. Use Swift Navigation supplied gerber data, fabrication drawing, and reference documents to fabricate printed circuit board (PCB)
- 3. Board shall conform to IPC-6011 and IPC-6012 class 2 specifications. Inspections and acceptance criteria per IPC-C-600, class 2
- 4. Complete netlist comparison shall be performed by fabricator prior to fabrication per IPC-9252 paragraph 5.3. Reference Swift Navigation suppled IPC-D-356 netlist files
- 5. 100% electrical test is required for shorts and continuity
- 6. Fabricator has approval to adjust all soldermask clearances to a maximum of 0.003"
- 7. Base material shall be NEMA FR-4 (UL94V-0) with minimum TG of 170C, use IPC-4101/126. Final printed circuit board must be RoHS compliant
- 8. Drill boards using electronic drill data, drill drawing, and drill chart. For holes not otherwise specified the hole tolerance shall not exceed +/- 0.003"
- after plating has been applied
- 10. PCB edges shall be clean cut without burrs
- 11. After the application of solder mask over bare copper (SMOBC) per IPC-SM-840, soldermask residue on surface mount pads is not permitted. No solder shall be permitted under solder mask contamination in excess of 10% of the total pad areas will be considered sufficient for lot rejection. All traces must be completely covered with soldermask.
- 12. Surface plating shall be electroless nickel immersion gold (ENIG)
- 13. Walls of plated thorugh holes shall be copper with a thickness of no less than 0.001"
- 14. No exposed copper shall be allowed
- 15. clear and legible white expoxy silkscreen required per supplied gerbers. Silkscreen to be removed from exposed pads as required
- 16. PCB Thickness 0.062" +/- 10%
- 17. Foil registration to be within 0.005" of supplied artwork when measured in reference to tooling hole or lower left corner. Front to back registration must be held to +/-0.005" and be concentric to all drilled holes within 0.005"
- 18 Soldermask color is blue, silkscreen color is white.

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Drill	Table	

Symbol	Count	Hole Size	Plated	Hole Tolerance
	22	0.76mm	Non-Plated	None
0	12	0.9mm	Plated	None
$\diamond$	20	1.02mm	Plated	None
X	12	0.99mm	Plated	None
¢	10	0.71mm	Plated	None
$\diamond$	8	3.26mm	Plated	None
<del>ل</del> ې	36	1.09mm	Plated	None
۲⁄۲	3	0.8mm	Plated	None
\$ <del>1</del> 5	2	2.3mm	Plated	None
$\bigtriangleup$	4	0.95mm	Plated	None
	2	1.2mm	Non-Plated	None
0	2	0.8mm	Non-Plated	None
$\diamond$	2	0.65mm	Plated	None
×	2	0.6mm	Plated	None
¢	2	0.85mm	Plated	None
\$	4	1.02mm	Plated	None
會	12	0.89mm	Plated	None
\$	2	1.57mm	Plated	None
资	2	3.25mm	Non-Plated	None
	4	3.5mm	Plated	None
•	4	3.5mm	Non-Plated	None
¢	201	0.2mm	Plated	None
•	218	0.25mm	Plated	None



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9. All hole and via dimensions on fab drawing and in drill chart are considered finished hole sizes

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Layer Stack Legend	Material	Layer	Thickness	Dielectric Material	Type	Gerbe
		Top Paste	THICK 1035		Paste Mask	GTP
	_	Top Overlay			Legend	GTO
	– Surface Material	Top Solder	0.01mm	Solder Resist	Solder Mask	GTS
	- Copper	Top Layer	0.051mm		Signal	GTL
	– Prepreg		0.069mm	370HR	Dielectric	
	– <b>Copper</b> – Core	GND	<b>0.03mm</b> 0.533mm	370HR	Internal Plane Dielectric	_
	— Copper	Signal Layer 1	0.03mm		Signal	G1
	– Prepreg		0.095mm	370HR	Dielectric	
	— <b>Copper</b> — Core	Signal Layer 2	<b>0.03mm</b> 0.533mm	370HR	<b>Signal</b> Dielectric	G2
	- Copper	PWR	0.03mm		Internal Plane	GP2
	– Prepreg		0.069mm	370HR	Dielectric	
	- Copper	Bottom Layer	0.051mm		Signal	GBL
	– Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
	_	Bottom Overlay			Legend	GBO
	_	Bottom Paste			Paste Mask	GBP

Total thickness: 1.543mm

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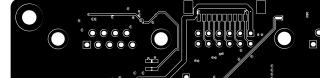
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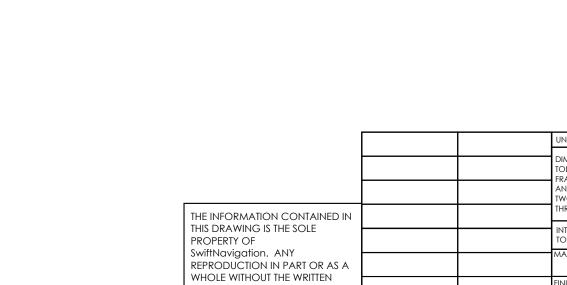
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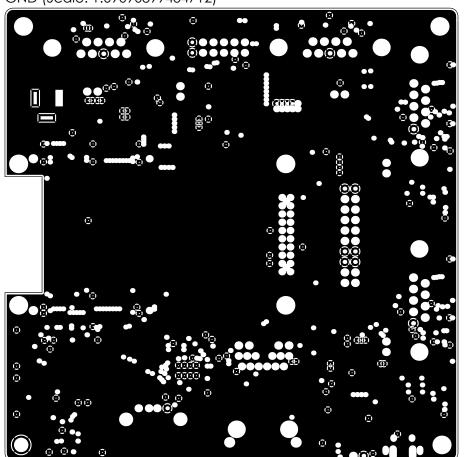
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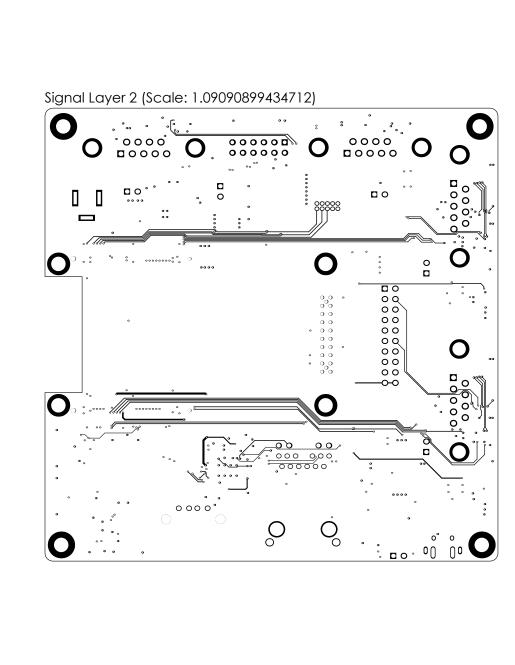


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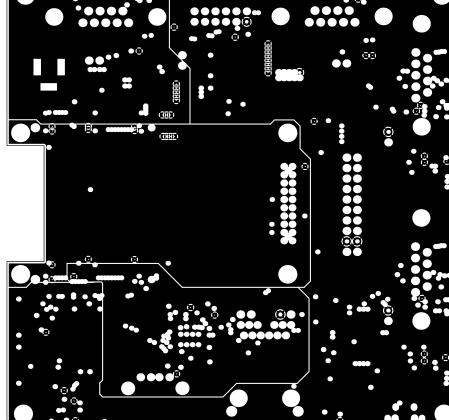
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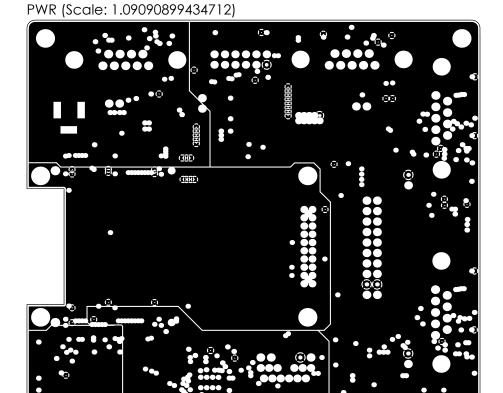
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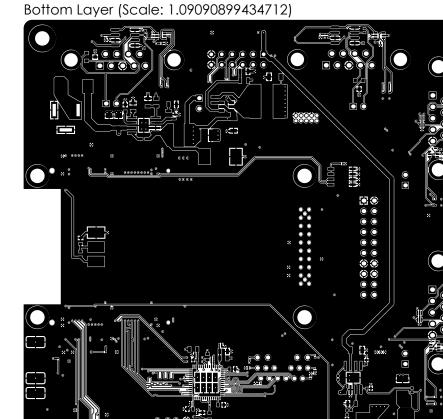
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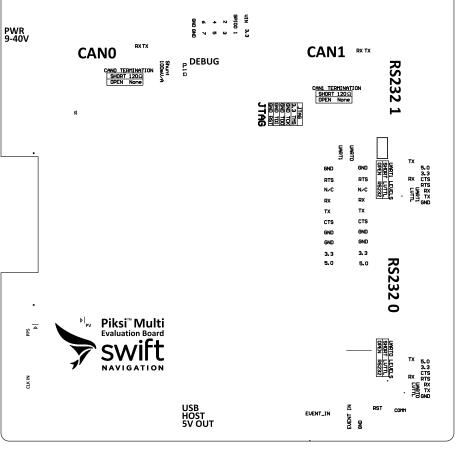


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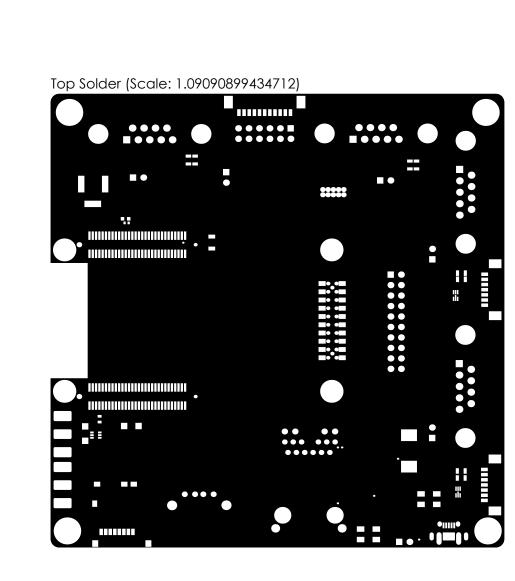
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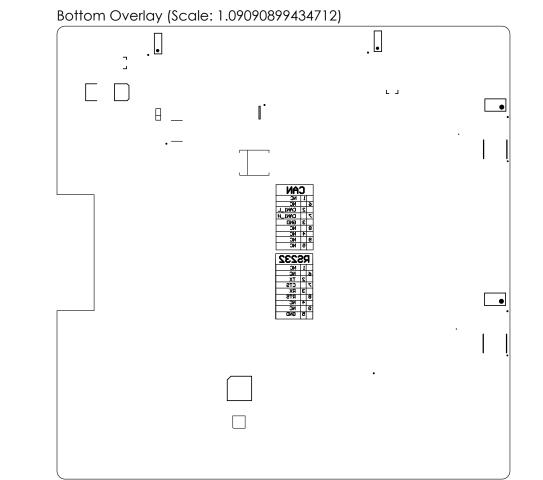
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